



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2728

SERIAL NUMBER 09/851,181 ✓	FILING DATE 05/08/2001 ✓ RULE	CLASS 709 370	GROUP ART UNIT 2661	ATTORNEY DOCKET NO. 01-035
-------------------------------	---	--------------------------------	------------------------	----------------------------------

APPLICANTS

Theodore F. Vaida, Lafayette, CO;
 Rajiv K. Singh, Broomfield, CO;
 Peter Gasperini, San Carlos, CA;

** CONTINUING DATA N/A TP *****

** FOREIGN APPLICATIONS N/A TP *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 06/29/2001

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CO ✓	SHEETS DRAWING 6 ✓	TOTAL CLAIMS 42 ✓	INDEPENDENT CLAIMS 3 ✓
---	--	-----------------------------	--------------------------	-------------------------	------------------------------

Verified and Acknowledged TRI H. PHAN TP
 Examiner's Signature Initials

ADDRESS
 24319 ✓
 LSI LOGIC CORPORATION
 1621 BARBER LANE
 MS: D-106
 MILPITAS, CA
 95035

TITLE
 Application specific integrated circuit having a programmable logic core and a method of operation thereof

FILING FEE RECEIVED 1106	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
------------------------------------	---	--